

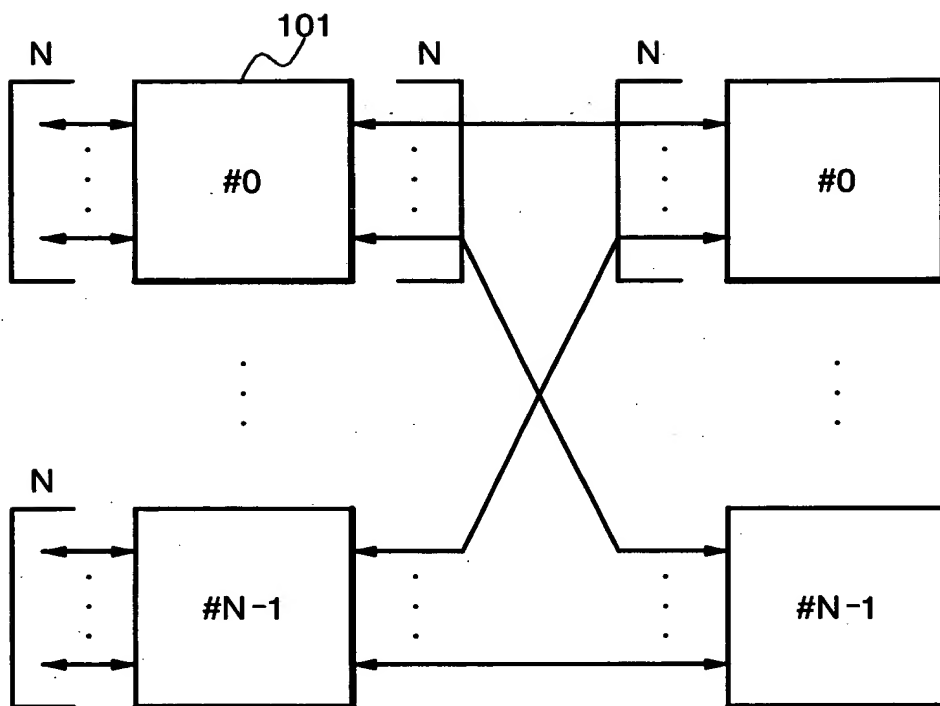
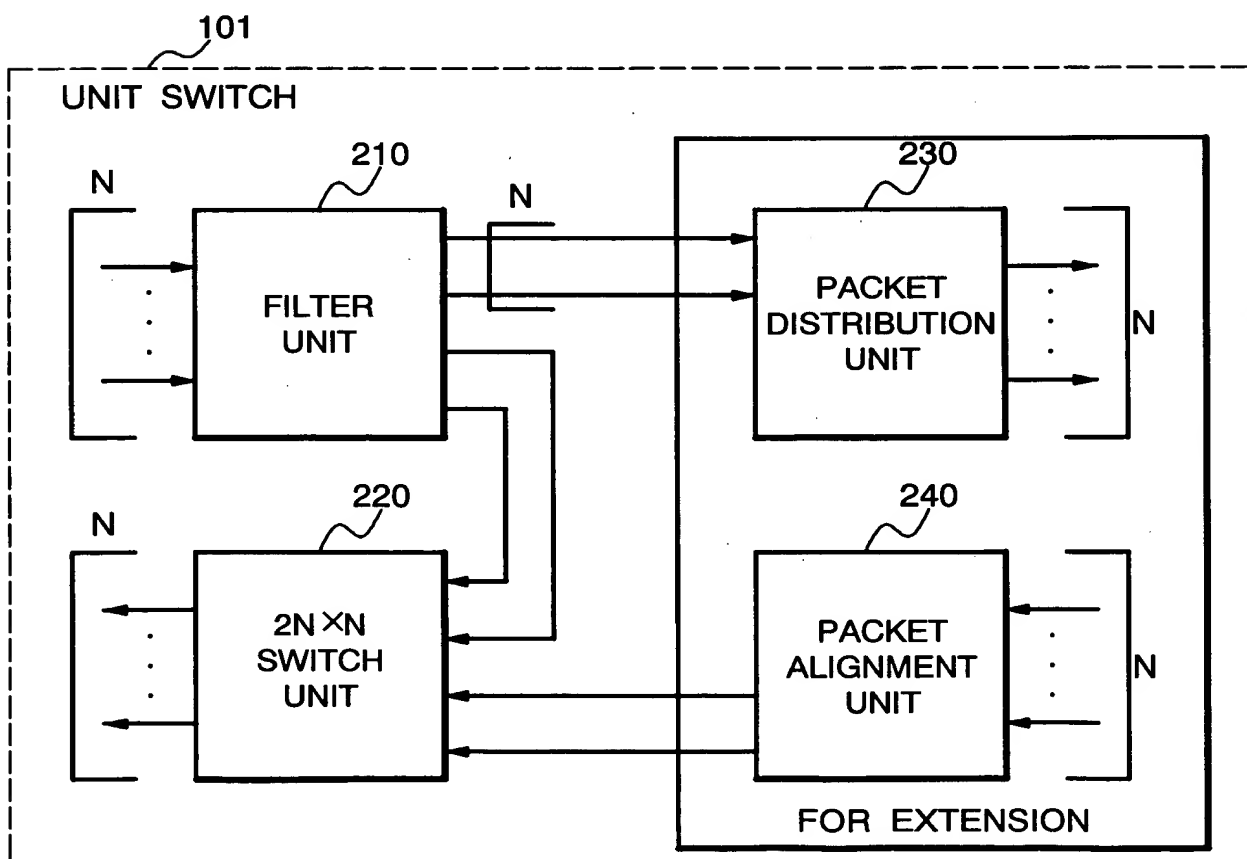
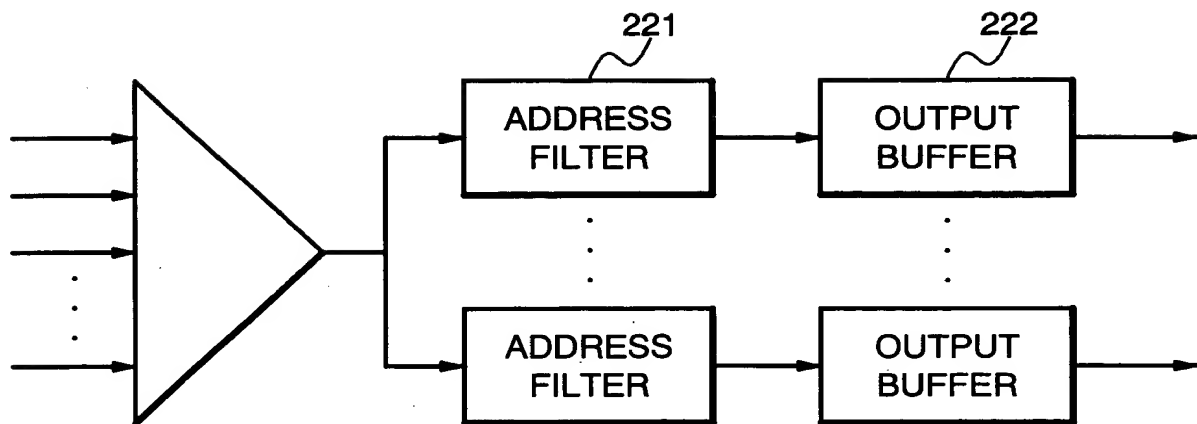
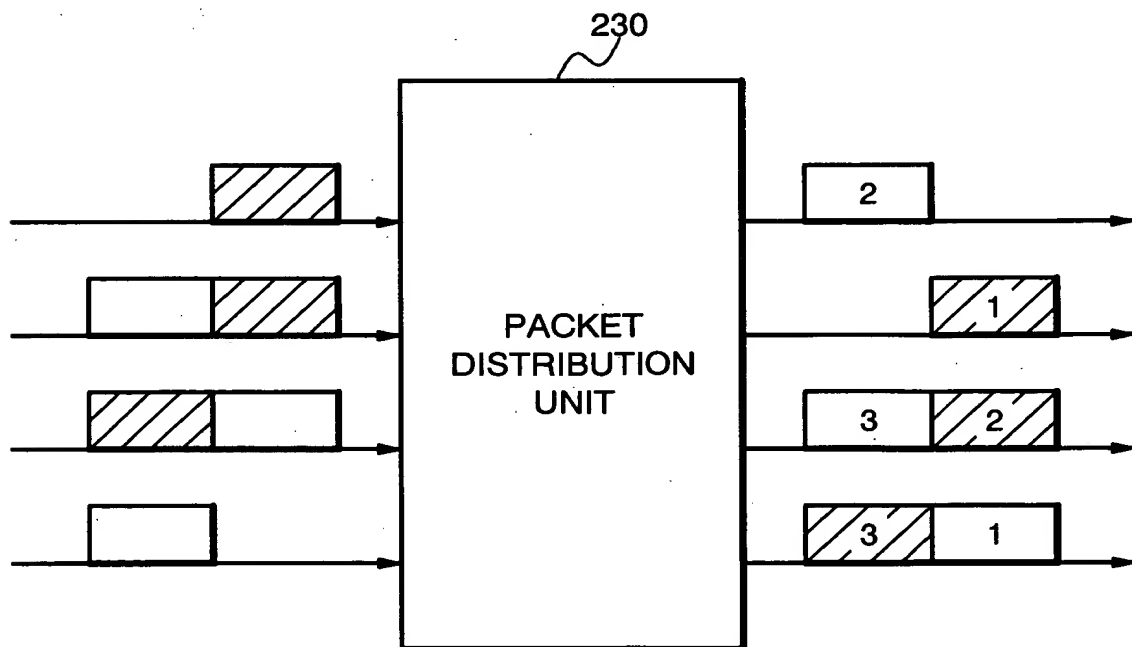
FIG. 1**FIG. 2**

FIG. 3**FIG. 4**



 **BOUND FOR SWITCH #1**
 **BOUND FOR SWITCH #N**

FIG. 5

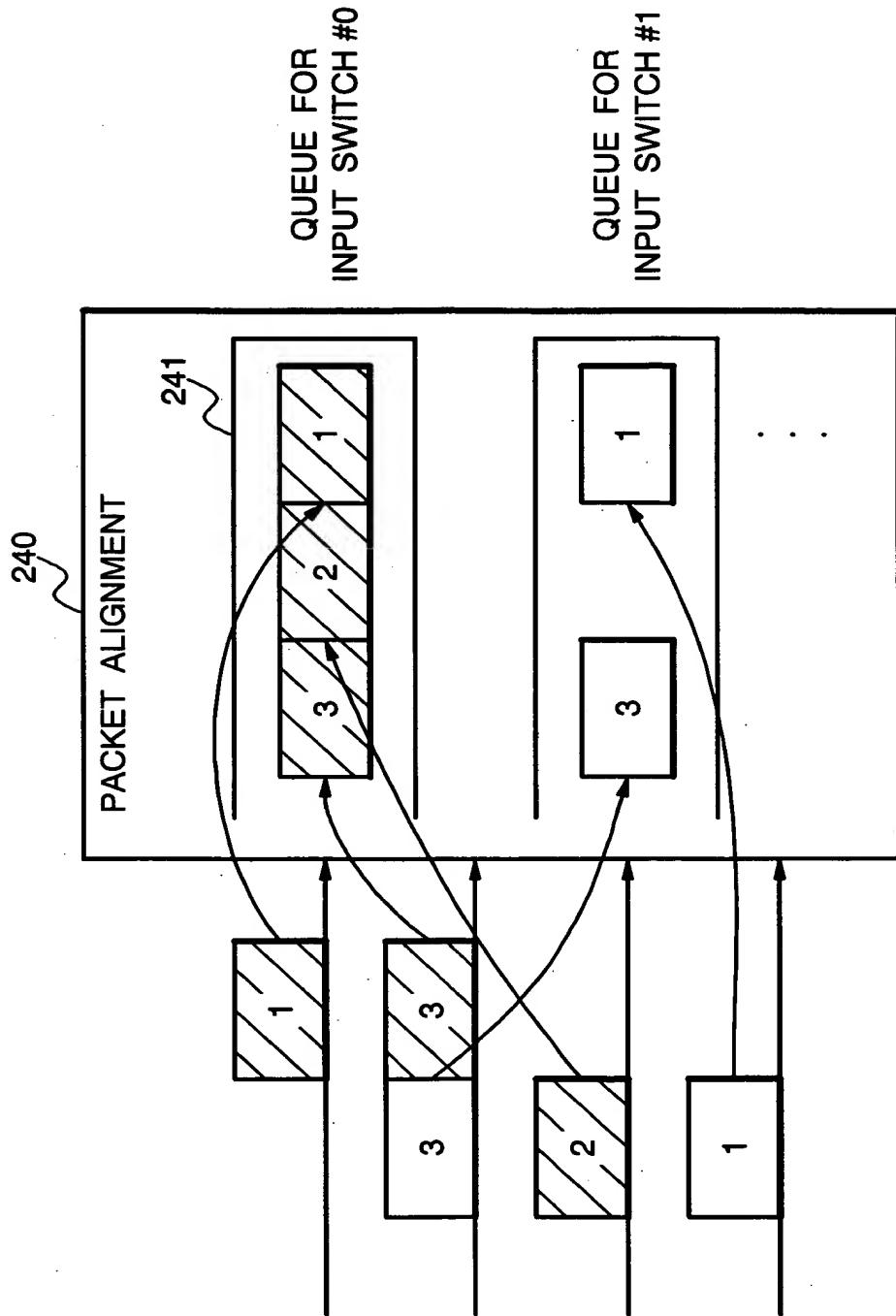


FIG. 6

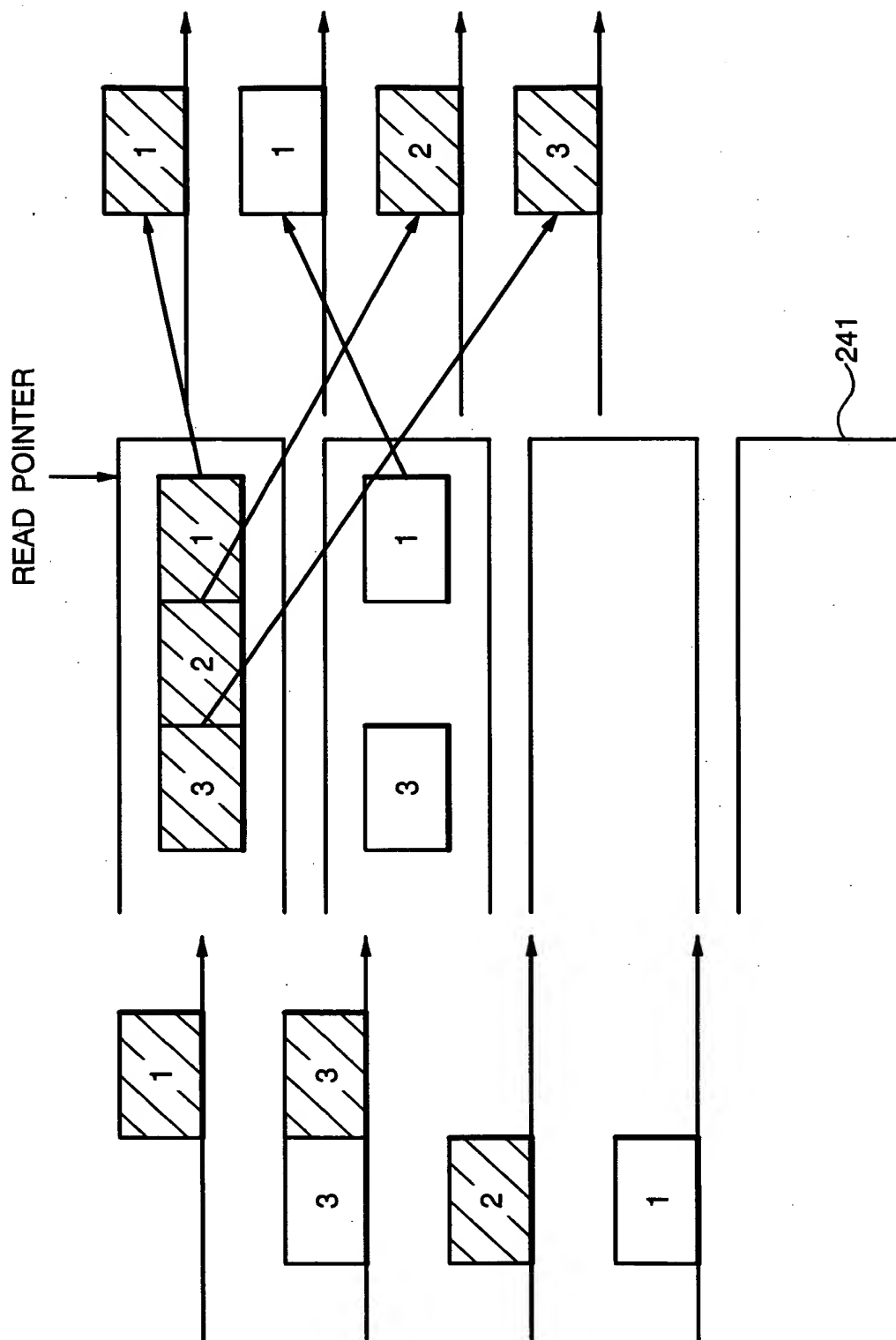
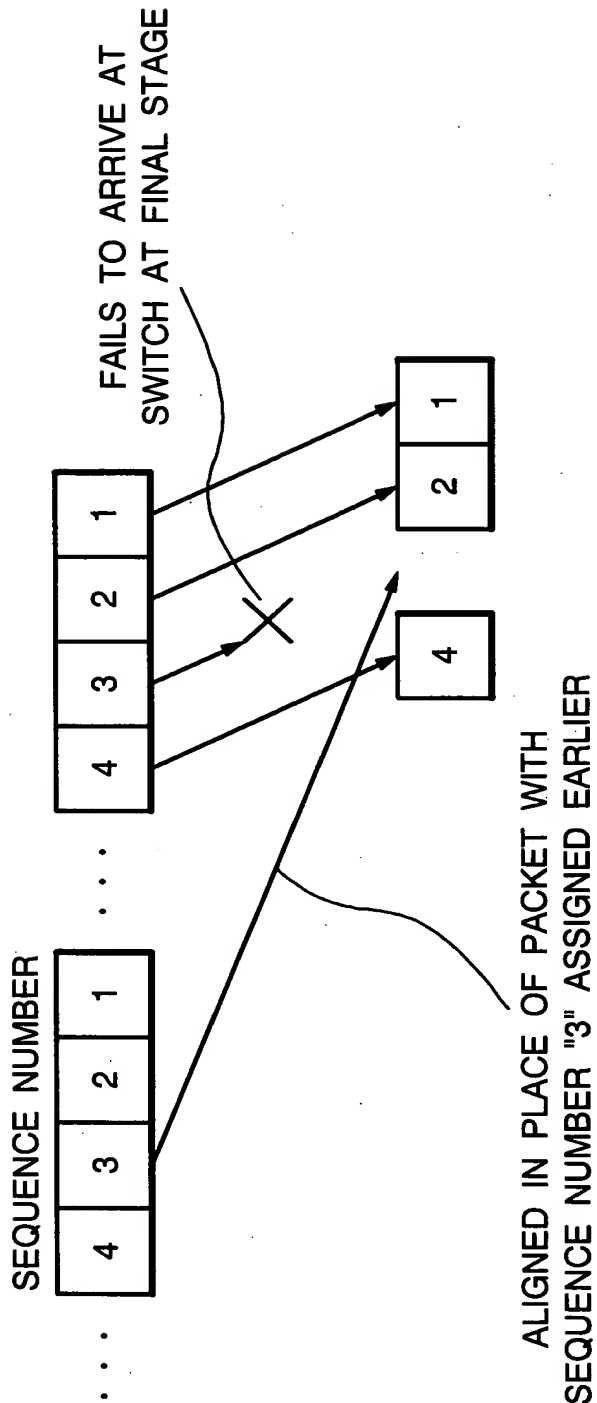


FIG. 7

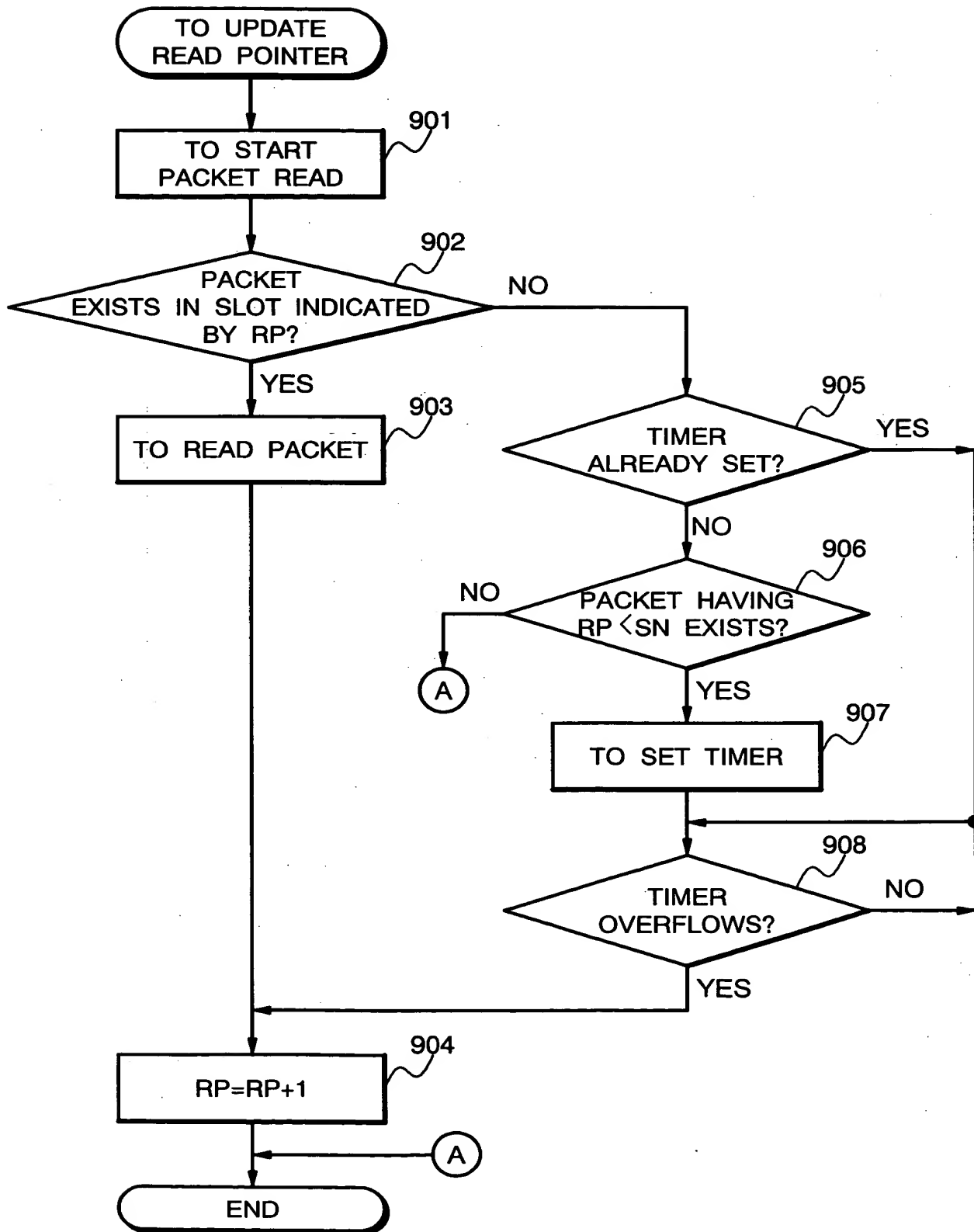


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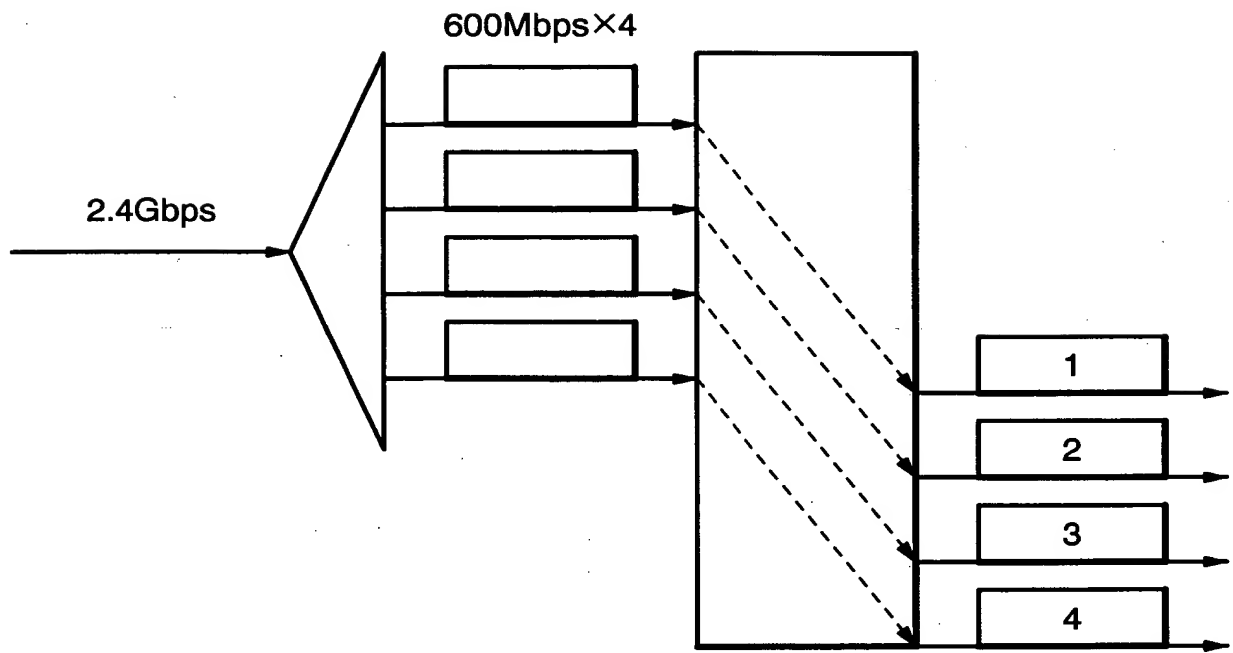
graph TD
    Start([TO SETTLE  
INITIAL VALUE OF  
READ POINTER]) --> 801[FIRST PACKET  
ARRIVES]
    801 --> 802{SN=0 ?}
    802 -- YES --> 803[RP=0]
    802 -- NO --> 804[RP=SN,  
TO SET TIMER]
    803 --> End([END  
( RP INITIAL  
VALUE SETTLED )])
    804 --> 805{TIMER  
OVERFLOWS?}
    805 -- YES --> End
    805 -- NO --> 806[PACKET ARRIVES]
    806 --> 807{RP > SN ?}
    807 -- YES --> End
    807 -- NO --> 804

```

FIG. 9



0011220" 06604560

FIG. 10

007EED" 06604560

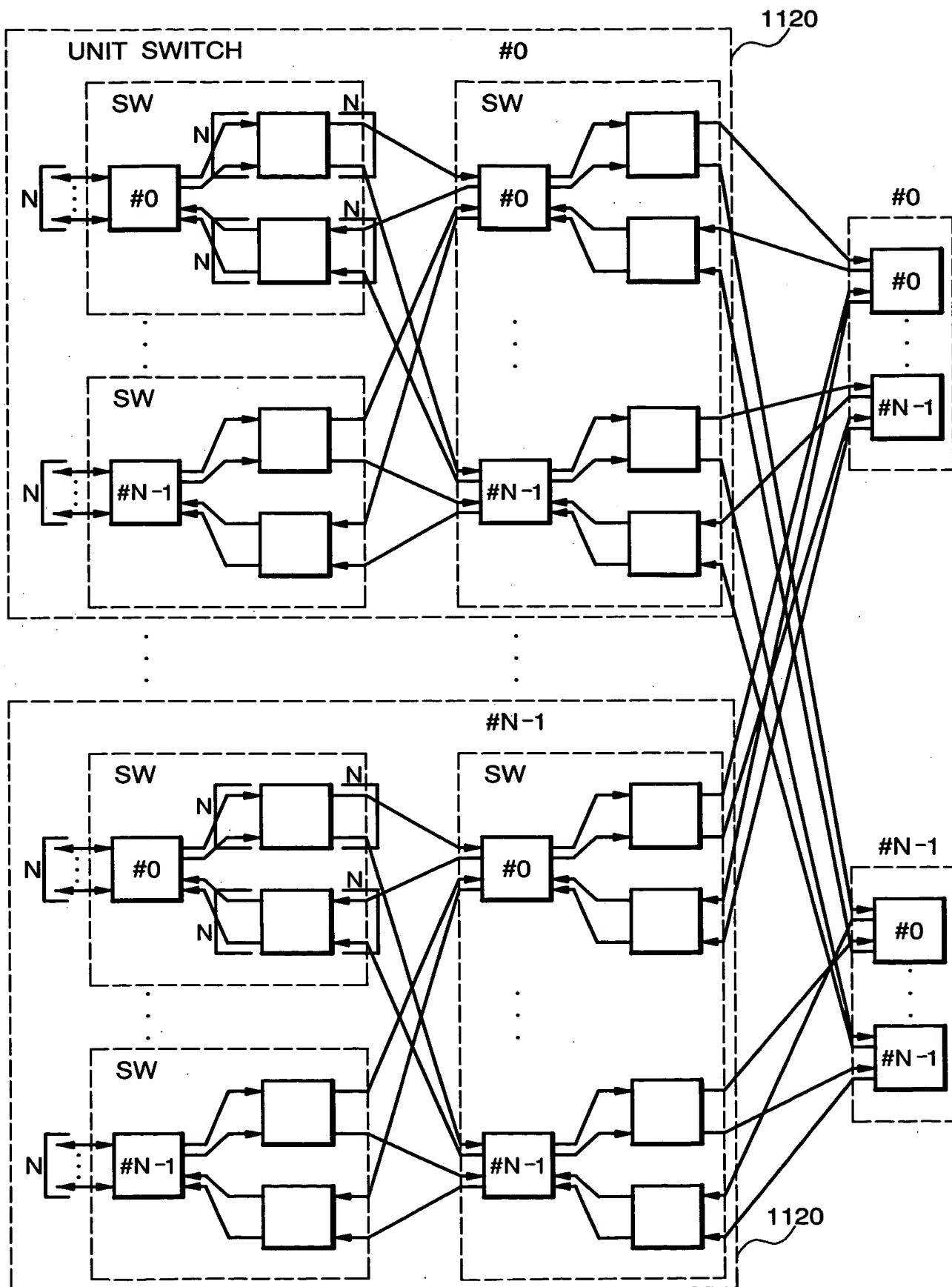
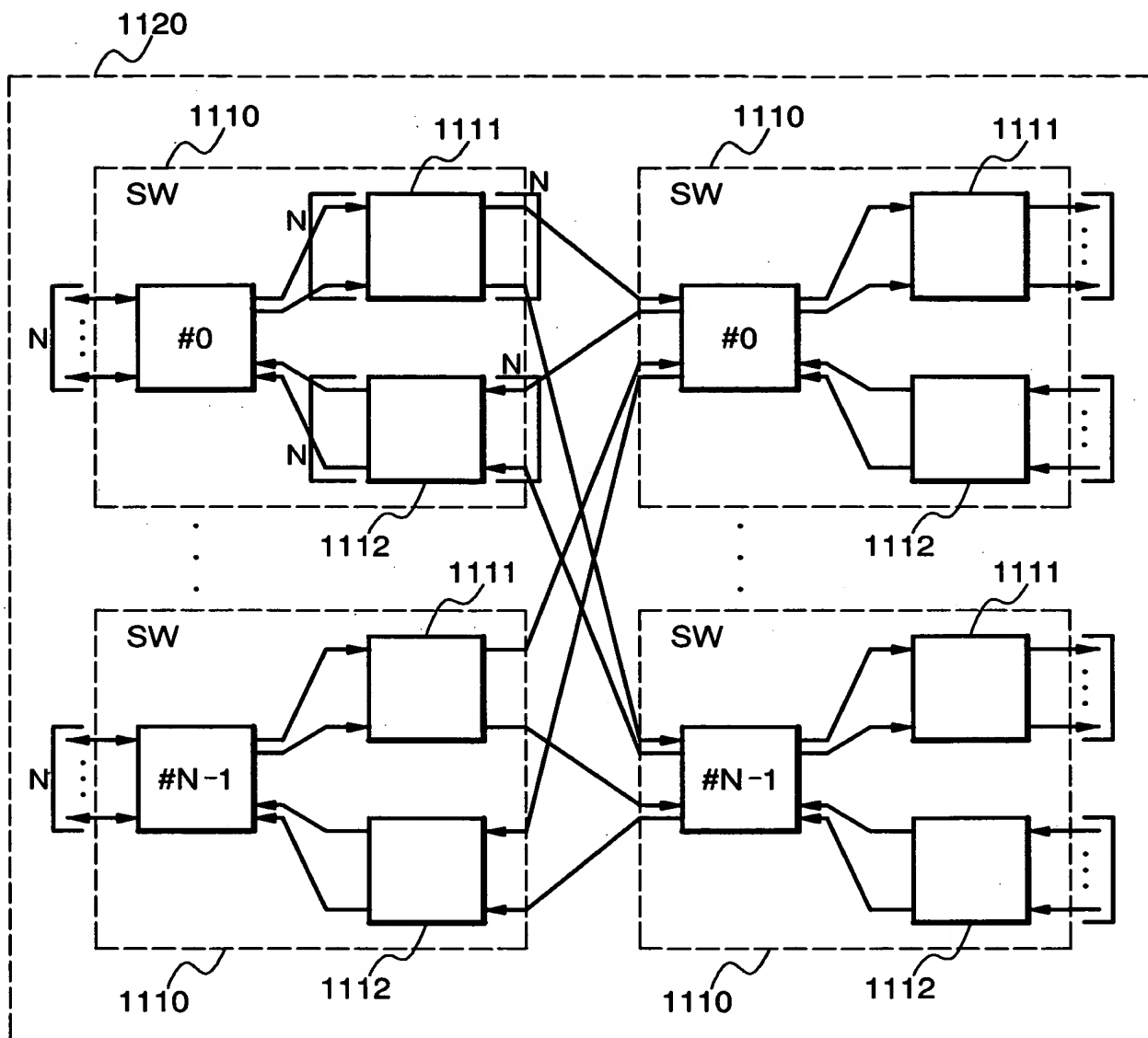
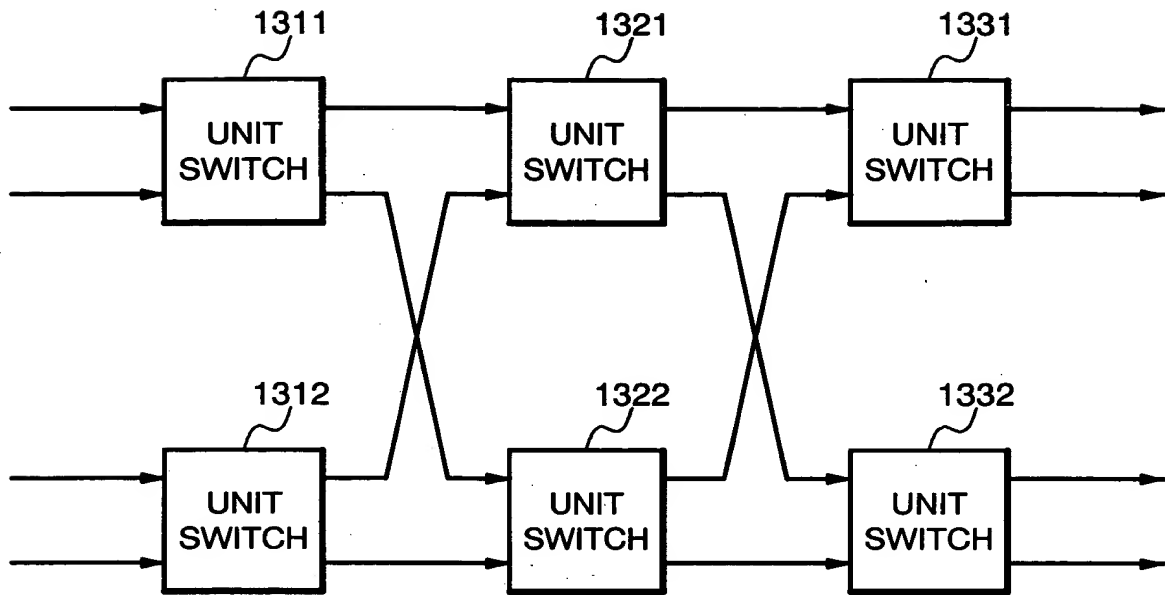
FIG. 11

FIG. 12

D01100 05601560

FIG. 13**FIG. 14**